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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**POWER OF ATTORNEY**

Docket No.  
03226.166001;P7131

Name of Applicant: **SUN MICROSYSTEMS, INC.**  
Address of Applicant: **901 San Antonio Road, MS-PAL01-521**  
**Palo Alto, California 94303**

Title: **CLOCK GRID SKEW REDUCTION TECHNIQUE USING**  
**BIASABLE DELAY DRIVERS**

Serial No., if Any: 10/082,892  
Filed: February 26, 2002

**RECEIVED**

**JUN 06 2002**

**TO THE ASSISTANT COMMISSIONER FOR PATENTS**

**Technology Center 2100**

The Assistant Commissioner for Patents  
Washington, D.C. 20231

Honorable Sir:

I hereby appoint:

Jonathan P. Osha, Reg. No. 33,986; Alan D. Rosenthal, Reg. No. 27,833; Richard A. Fagin, Reg. No. 39,182; Thomas K. Scherer, Reg. No. 45,079; Scott W. Hejny, Reg. No. 45,882; Y. Renee Alsandor, Reg. No. 45,883; Jeffrey S. Bergman, Reg. No. 45,925; Robert P. Lord, Reg. No. 46,479; T. Chyau Liang, Reg. No. 48,885; Wasif H. Qureshi, Reg. No. 51,048; James L. Kurka, Reg. No. 47,726, of ROSENTHAL & OSHA L.L.P.; and

Kenneth Olsen, Reg. No. 26,493; Timothy J. Crean, Reg. No. 37,116; Alexander E. Silverman, Reg. No. 37,940; Anirma Rakshpal Gupta, Reg. No. 38,275; Sean P. Lewis, Reg. No. 42,798; Michael J. Schallop, Reg. No. 44,319; Bernice B. Chen, Reg. No. 42,403; Noreen A. Krall, Reg. No. 39,734; Monica D. Ward, Reg. No. 40,696; Marc D. Foodman, Reg. No. 34,110; Elaine K. Lee, Reg. No. 41,936; Hugh H. Matsubayashi, Reg. No. 43,779; Paul D. Sorkin, Reg. No. 39,039; Marilyn E. Glaubenslee, Reg. No. 35,521; Andrew C. Chen, Reg. No. 43,544; Jeffrey L. Myers, Reg. No. 44,252, and Pavel Pogodin, Reg. No. P48,205 of SUN MICROSYSTEMS, INC.

as principal attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

Please direct all future correspondence to:

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Dated: March 26, 2002



12182

**TRANSMITTAL LETTER  
(General - Patent Pending)**

Docket No.  
03226/166001;P7131

COPY OF PAPERS  
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In Re Application Of: **Pradeep TRIVEDI et al.**

Serial No.  
10/082,892

Filing Date  
February 26, 2002

Examiner

Group Art Unit  
2182

Title: **CLOCK GRID SKEW REDUCTION TECHNIQUE USING BIASABLE DELAY DRIVERS**

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Transmitted herewith is:

**Power of Attorney, 1 page**

in the above identified application.

- ☒ No additional fee is required.
- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. **50-0591** as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of \_\_\_\_\_
- ☐ Credit any overpayment.
- ☒ Charge any additional fee required.

*Signature*

Dated:

5/16/02

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I certify that this document and fee is being deposited on **5/16/02** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

*Signature of Person Mailing Correspondence*

**Tawana L. Garcia**

*Typed or Printed Name of Person Mailing Correspondence*

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